



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

\$ ATTORNEY DOCKET NO.
\$ JP920000113US1

SHUJI YAMADA §

Serial No. 09/838,994 § Examiner: UNKNOWN

Filed: **April 20, 2001** § Group Art Unit: **2182**

For: SYSTEM AND METHOD OF
IMPLEMENTING A BUFFER MEMORY
AND HARD DISK DRIVE WRITE
CONTROLLER

\$

LETTER TO THE OFFICIAL DRAFTSMAN

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Submitted herewith are sixteen (16) sheets of formal drawings for the above-identified application.

CERTIFICATE OF MAILING 37 C.F.R. § 1.8(A)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to Assistant Commissioner of Patents and Trademarks, Vashington, O. C. 20231.

2/16/01

Date

Respectfully submitted,

Andrew J. Dillon

Registration No. 29,634

BRACEWELL & PATTERSON, L.L.P.

Suite 350, Lakewood on the Park

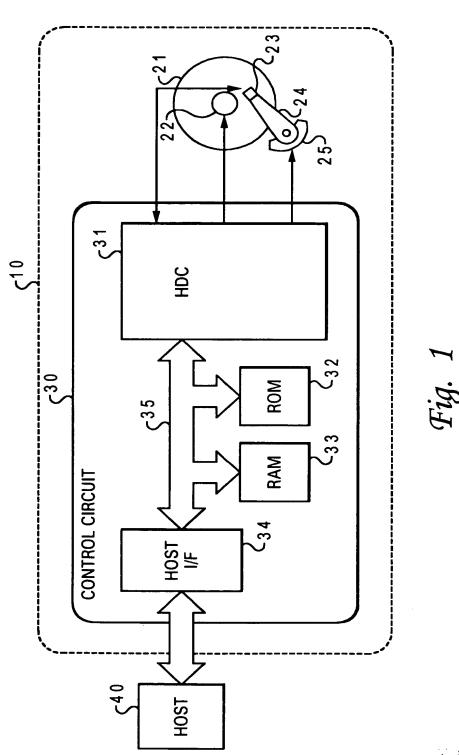
7600B North Capital of Texas Highway

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ATTORNEY FOR APPLICANT

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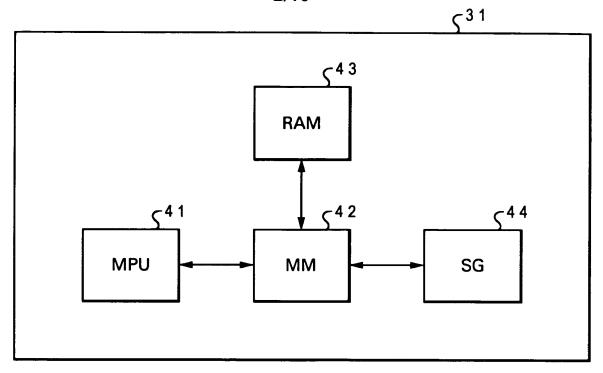
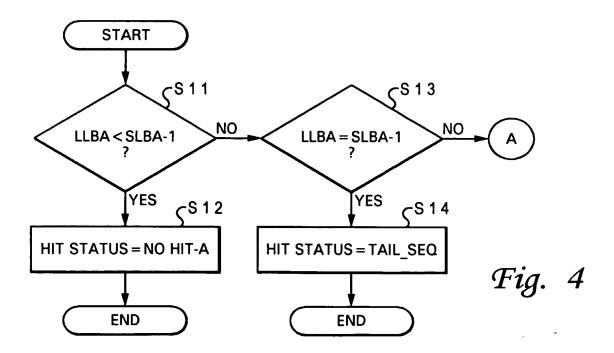
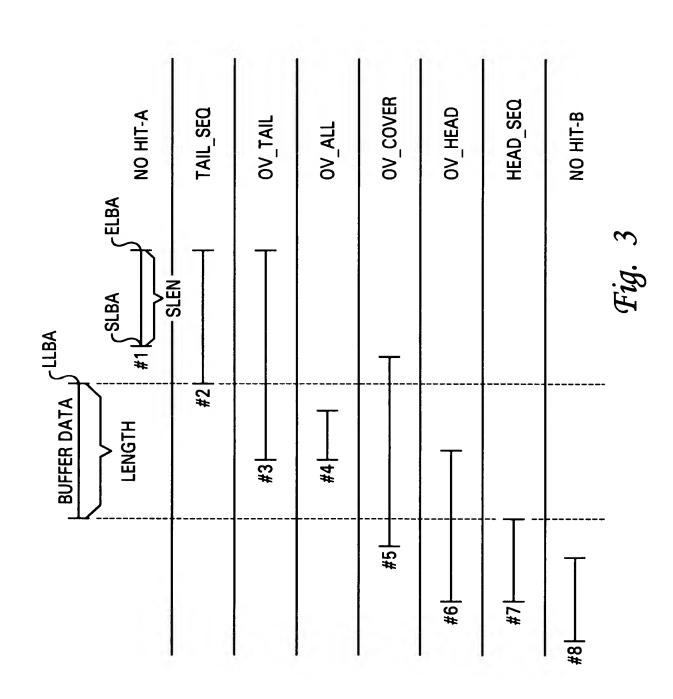


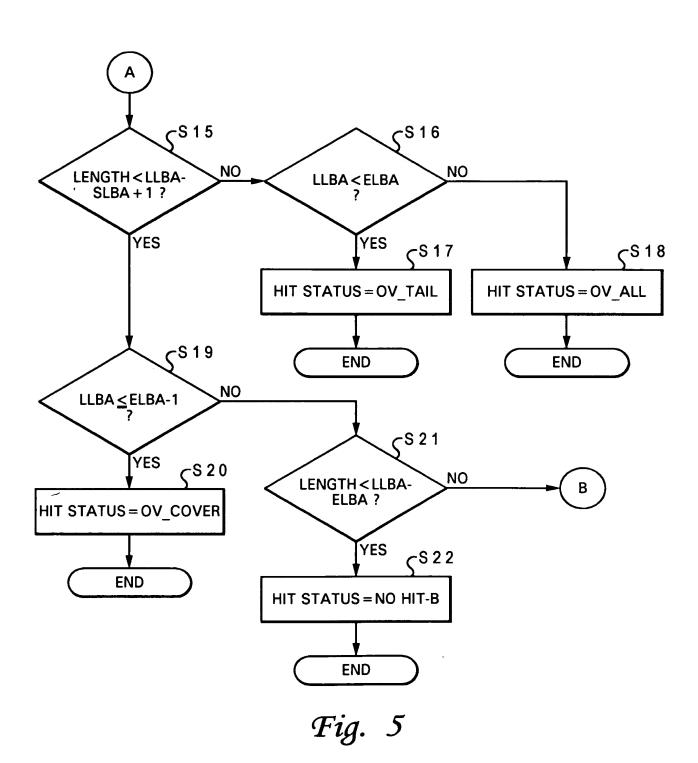
Fig. 2



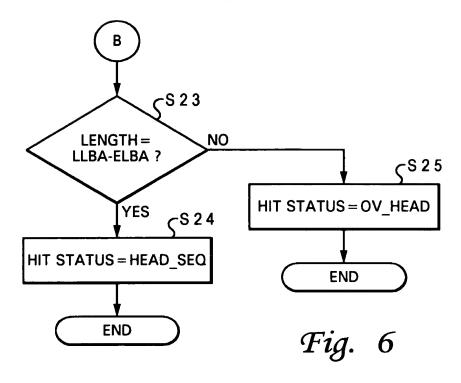
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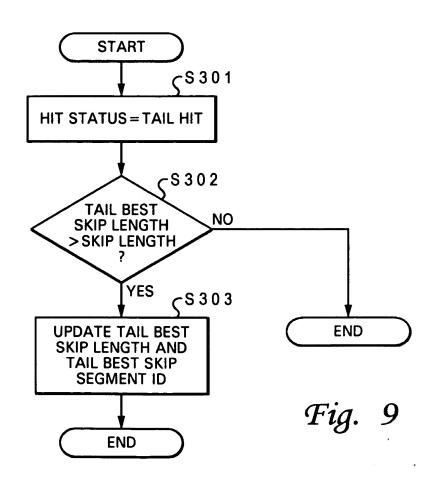


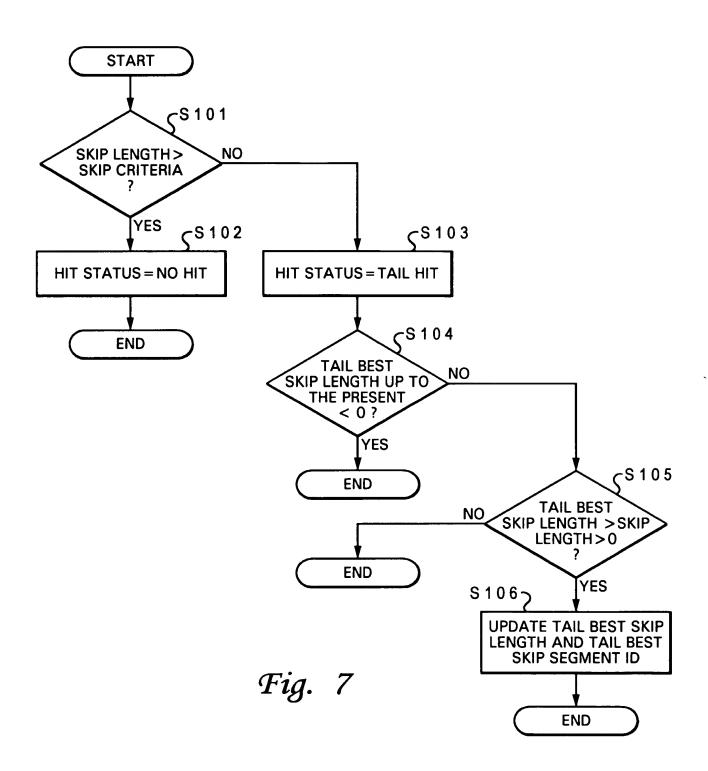
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*SKIP LENGTH = 0

JP920000113US1 Shuji Yamada 7/16 **START** CS 201 HIT STATUS = TAIL HIT CS 202 CS 203 **TAIL** TAIL NO NO BEST SKIP LENGTH **BEST SKIP LENGTH** >SKIP LENGTH **SO FAR < 0** YES YES CS 204 **END UPDATE TAIL BEST SKIP END** LENGTH AND TAIL BEST SKIP SEGMENT ID Fig. 8 **END START** CS701 HIT STATUS = HEAD HIT S702 S703 **HEAD BEST HEAD BEST** NO NO SKIP LENGTH UP TO SKIP LENGTH> THE PRESENT SKIP LENGTH <0? YES YES cS704 **END UPDATE HEAD BEST END** SKIP LENGTH AND HEAD **BEST SKIP SEGMENT ID** Fig. 14

END

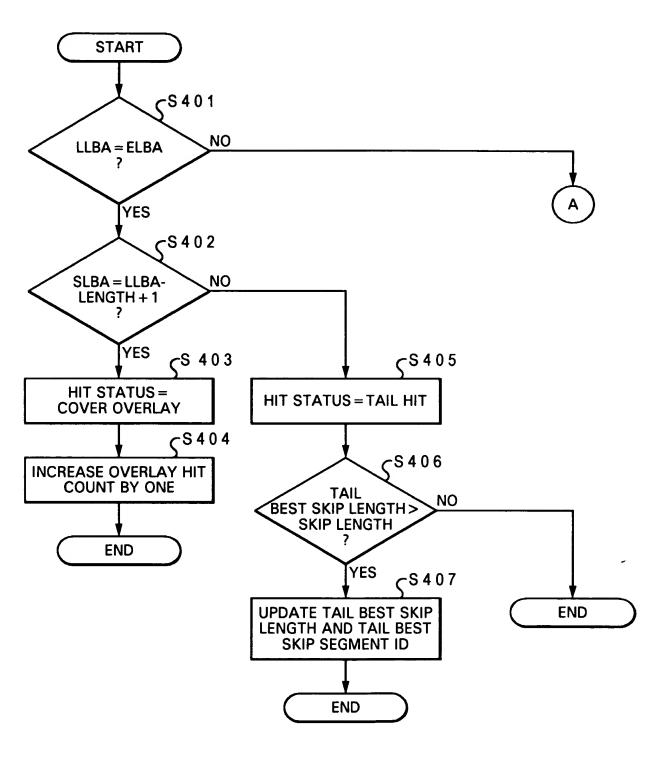


Fig. 10

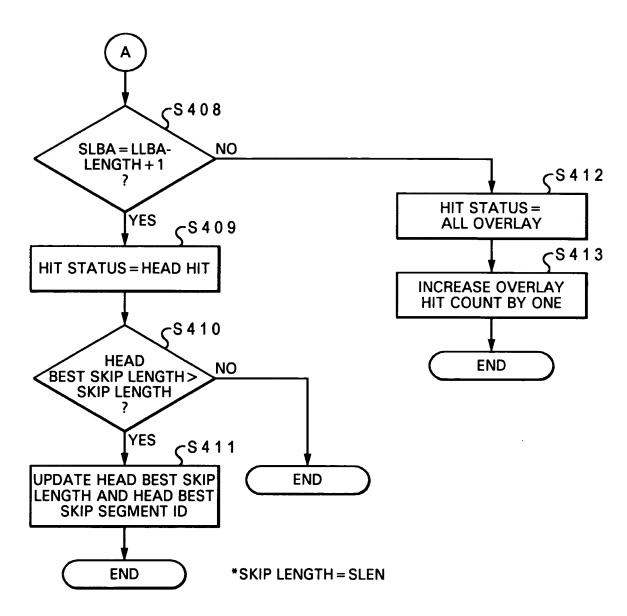
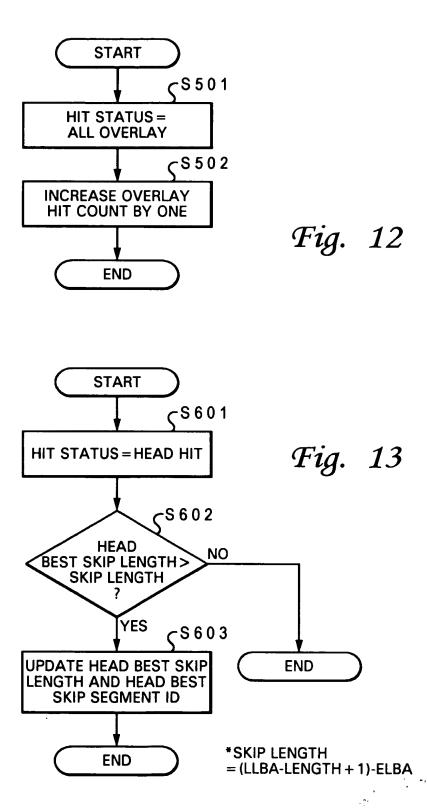


Fig. 11



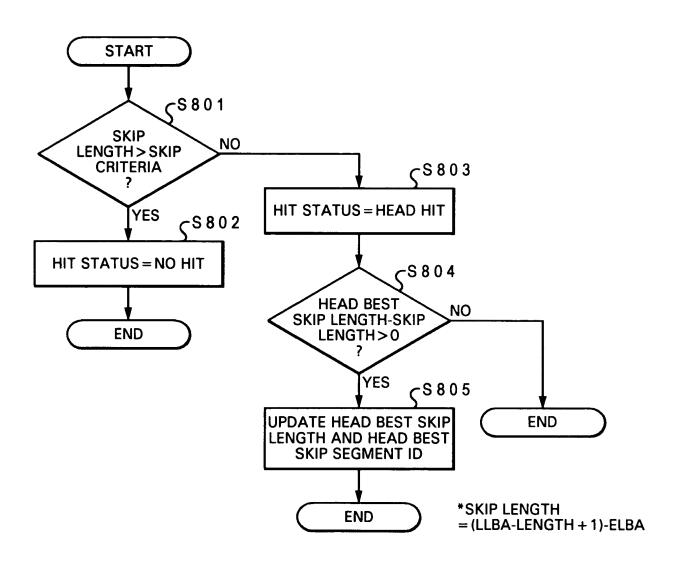


Fig. 15

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H/W USE	H/W USE	H/W USE	NO OF HIT
HEAD BEST SKIP	HEAD BEST SKIP	TAIL BEST SKIP	TAIL BEST SKIP
LENGTH	SEGMENT ID	LENGTH	SEGMENT ID

Fig. 16

LAST LBA LOW

LAST LBA HIGH

LENGTH

HIT STATUS

Fig. 17

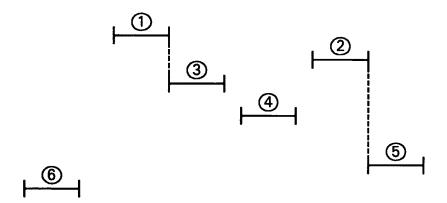


Fig. 18a

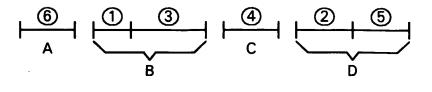


Fig. 186

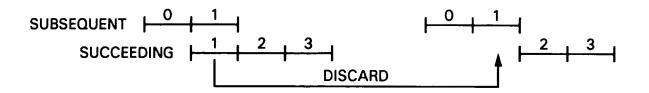


Fig. 19

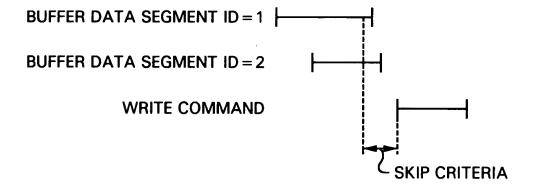


Fig. 20

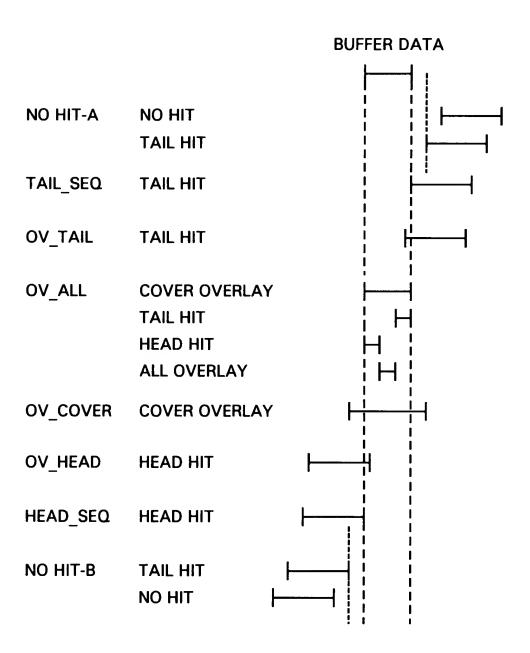
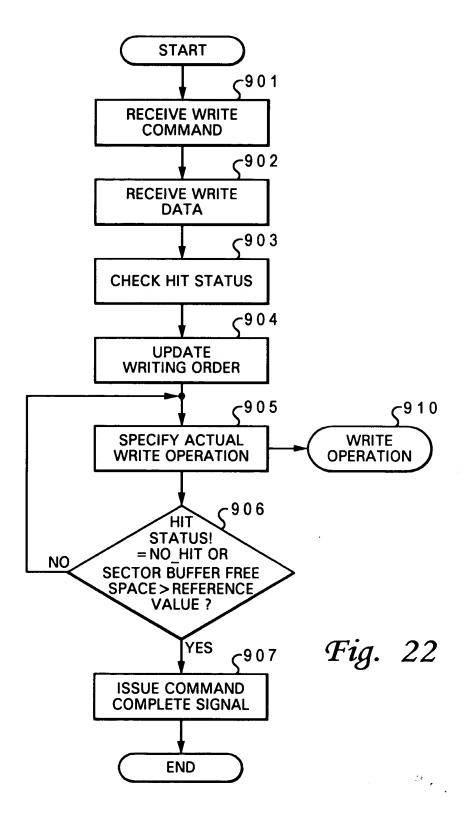


Fig. 21

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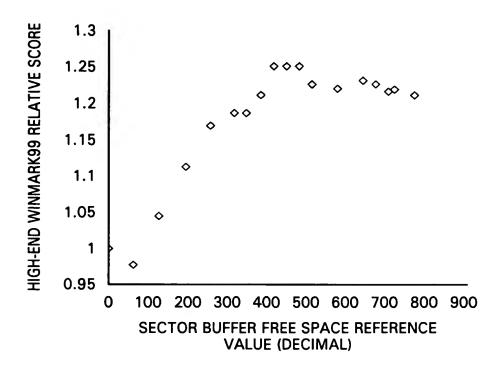


Fig. 23

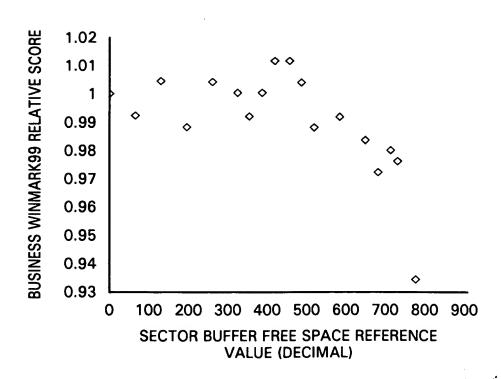


Fig. 24

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